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		Number of Pages, Including Cover:	4
Re:	Serial No. 10/664,875		

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U.S. practice conducted through McDermott Will & Emery LLP.

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Applicant: Naofaru MATSUBARA, et al. Docket No. 68933-044

Title: SEMICONDUCTOR DEVICE INCLUDING INTERCONNECTS FORMED BY DAMASCENE PROCESS AND MANUFACTURING METHOD THEREOF

Date Sent: 8/12/2004 ☒ Hand Carried ☐ Fax ☐ Electronic ☐ Cent. of Mailing ☐ First Class Mail ☐ Express Mail No. 10/664,875

☐ Transmittal Letter ☐ New Patent App ☐ Utility ☐ Design ☐ Cont. ☐ CIP ☐ Div. ☐ PCT ☐ RCE ☐ Prov

Other: _____

_____ pages of Specification
_____ pages of Claims
_____ pages of Abstract
_____ pages of Formal/Informal Drawings

☐ Small Entity ☐ Large Entity
☐ Declaration/Power of Attorney
☐ Recordation of Assignment/Security Agreement
☐ Information Disclosure Statement

_____ Form PTO 1449
_____ copies of cited references

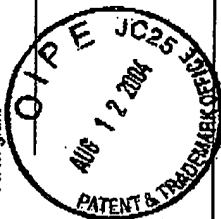
☐ Preliminary Amendment
☐ Response to Missing Parts Notice
☐ Resp. to Notice to Correct App. Papers
☐ Certified Copy of Priority Doc.
☐ Claim for Convention Priority
☒ Response/Amendment to Office Action of _____ July 14, 2004
☐ Request for _____ month Extension of Time

☐ Letter submitting _____ pages of drawings
☐ Req. for Approval of Drawing Amendments
☐ Req. for Oral Hearing
☐ Not. of Appeal ☐ Appeal Brief ☐ Reply Brief
☐ Rule 312 Amendment/Letter
☐ Req. for Acknowledgement of Cited Art
☐ Issue Fee
☐ Publication Fee
☐ Req. for Certificate of Correction
☐ Maintenance Fee for _____ years after grant
☐ Fee Address Indication Form
☐ Terminal Disclaimer
☐ Petition to Commissioner
☐ Status Inquiry
☐ Other _____

Check for \$ _____ Charge Deposit Acct. 5004173 Atty. Init. AJS Tkpt. # 4236 Secy. or PL NBelt

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THE PATENT AND TRADEMARK OFFICE DATE STAMPED HEREON IS ACKNOWLEDGEMENT THAT THE ITEMS, CHECKED ABOVE, WERE RECEIVED BY THE PTO ON THE DATE STAMPED.



Docket No.: 65933-044

PATENT**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of : Customer Number: 20277
Naoteru MATSUBARA, et al. : Confirmation Number: 4231
Serial No.: 10/664,875 : Group Art Unit: 2811
Filed: September 22, 2003 : Examiner: J. Im
For: SEMICONDUCTOR DEVICE INCLUDING INTERCONNECTS FORMED BY
DAMASCENE PROCESS AND MANUFACTURING METHOD THEREOF

RESPONSE TO RESTRICTION REQUIREMENT

Mail Stop Restriction Requirement
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In the Office Action dated July 14, 2004, the Examiner imposed a Restriction Requirement pursuant to 35 U.S.C. §121, requiring election of either:

- Group I. Claims 1 through 5, directed a semiconductor device; or
- Group II. Claims 6 through 17, directed to a method.

In response Applicants elect to process device claims 1 through 5 (Group I). Favorable consideration of claims 1 through 5 is solicited. Applicants further solicit rejoinder of non-elected method claims 6 through 17, upon the determination of an allowable device claim, pursuant to the provisions of MPEP§821.04.


WDC99 962809-1.065933.0044

10/664,875

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

MCDERMOTT WILL & EMERY LLP


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Date: August 12, 2004